

FORM PTO-1390 (REV. 11-2000)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY'S DOCKET NUMBER <b>450101-02582</b>
<b>TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371</b>			U.S. APPLICATION NO. (If known see 37 C.F.R. 1.5) <b>097763760</b>
INTERNATIONAL APPLICATION NO. <b>PCT/JP00/04375</b>	INTERNATIONAL FILING DATE <b>30 JUNE 2000</b>	PRIORITY DATE CLAIMED <b>30 JUNE 1999</b>	
TITLE OF INVENTION <b>TRANSCODING APPARATUS, METHOD AND MEDIUM</b>			
APPLICANT(S) FOR DO/EO/US <b>Motoki KATO</b>			
<p>Applicants herewith submit to the United States Designated/Elected Office (DO/EO/US) the following items and other information:</p> <ol style="list-style-type: none"> <li><input checked="" type="checkbox"/> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. 371.</li> <li><input type="checkbox"/> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 35 U.S.C. 371.</li> <li><input checked="" type="checkbox"/> This is an express request to promptly begin national examination procedures (35 U.S.C. 371(f)).</li> <li><input type="checkbox"/> The US has been elected by the expiration of 19 months from the priority date (PCT Article 31).</li> <li><input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371(c)(2))             <ol style="list-style-type: none"> <li><input type="checkbox"/> is attached hereto (required only if not communicated by the International Bureau).</li> <li><input checked="" type="checkbox"/> has been communicated by the International Bureau.</li> <li><input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US).</li> </ol> </li> <li><input checked="" type="checkbox"/> An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)).</li> <li><input checked="" type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))             <ol style="list-style-type: none"> <li><input type="checkbox"/> are attached hereto (required only if not communicated by the International Bureau).</li> <li><input type="checkbox"/> have been communicated by the International Bureau.</li> <li><input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired.</li> <li><input checked="" type="checkbox"/> have not been made and will not be made.</li> </ol> </li> <li><input type="checkbox"/> A English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).</li> <li><input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).</li> <li><input type="checkbox"/> An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).</li> </ol> <p><b>Items 11 to 20 below concern document(s) or information included:</b></p> <ol style="list-style-type: none"> <li><input checked="" type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.</li> <li><input checked="" type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.</li> <li><input type="checkbox"/> A FIRST preliminary amendment.</li> <li><input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment.</li> <li><input type="checkbox"/> A substitute specification.</li> <li><input type="checkbox"/> A change of power of attorney and/or address letter.</li> <li><input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825.</li> <li><input type="checkbox"/> A second copy of the published international application under 35 U.S.C. 154(d)(4).</li> <li><input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).</li> <li><input checked="" type="checkbox"/> Other items or information:  PCT/IB/301, PCT/ISA/210  PCT/IB/304, PCT-EAST Version 2.90  PCT/IB/308 </li> </ol>			

**EXPRESS MAIL**

Mailing Label Number: **EL742670031US**

Date of Deposit: **February 26, 2001**

I hereby certify that this paper or fee is being deposited with the United States Postal Service

"Express Mail Post Office to Addressee" Service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents and Trademarks, Box PCT Washington, DC 20231.

*Kenneth G. Hays*  
(Typed or printed name of person filing paper or fee)

(Signature of person mailing paper or fee)

Form PTO-1390 (REV 11-2000) page 2 of 2

## DESCRIPTION

## Transcoding Apparatus, Method and Medium

## Technical Field

The present invention relates to a transcoding apparatus, method and medium, and more particularly to a transcoding apparatus, method and medium, for converting codes used in a transport stream having multiplexed therein a video stream, audio stream and other data stream.

## Background Art

In the satellite digital television broadcasting and the ground-wave digital television broadcasting which is going to start soon, a transport stream (will be referred to simply as "TS" hereinafter) conforming to the MPEG (Moving Pictures coding Experts Group) 2 systems (ISO/IEC 13818-1) (will be referred to as "MPEG-2" hereinafter), is used as the broadcast wave. TS as the broadcast wave has multiplexed therein a video stream, audio stream and other data stream.

When TS as the digital television broadcast wave is recorded into a recording medium, a bit rate changer as shown in FIG. 1 is used to reduce the code bit rate for a to-be-recorded TS if the data recording rate into the recording medium is lower than at a bit rate at which the TS is to be recorded. Note that since the bit rate changer functions to convert the code bit rate, it is a kind of transcoder for converting codes of an input stream.

A TS demultiplexer 1 included in the bit rate changer shown in FIG. 1 separates an input TS via an input terminal IN into a video stream VS and audio stream AS and supplies them to an MPEG video decoder 2 and MPEG audio decoder 4, respectively.

The MPEG video decoder 2 decodes the supplied video stream VS to provide a video signal and supplies the video signal to an MPEG video encoder 3. The MPEG video encoder 3 will encode the supplied video signal to a video stream having a predetermined bit rate by the MPEG method, and supply the encoded video stream to a TS multiplexer 6. On the other hand, the MPEG audio decoder 4 decodes the supplied audio stream AS supplied to provide an audio signal and supplies the audio signal to an MPEG audio encoder 5. The MPEG audio encoder 5 will encode the supplied audio signal to an audio stream having a predetermined bit rate by the MPEG method, and supply the coded audio stream to the TS multiplexer 6. The TS multiplexer 6 will multiplex the video stream supplied from the MPEG video encoder 3 and the audio stream supplied from the MPEG audio encoder 5 to provide a TS' having a reduced bit rate. The TS' is delivered at an output terminal OUT.

However, the conventional bit rate changer shown in FIG. 1 is disadvantageous in that when an input TS contains any elementary streams not conforming to MPEG-2, the TS, once separated into elementary streams, cannot be re-multiplexed. For example, a private stream such as a data stream used in the digital television broadcasting is not conforming to MPEG-2, and each of service providers uniquely sets necessary buffer size and bit rate for multiplexing. No multiplexing is possible

when the TS multiplexer 6 does not understand the buffer model for a stream to be multiplexed. Therefore, it is difficult to multiplex a private stream such as data stream for example except when the buffer model of the stream is already known.

Also, the conventional bit rate changer shown in FIG. 1 has a problem that it can change the bit rate of a transport stream TS having multiplexed therein one video stream VS and one audio stream AS, each conforming to MPEG-2 but cannot change the bit rate of a TS having multiplexed therein one video stream VS and a plurality of audio streams AS.

#### Disclosure of the Invention

Accordingly, the present invention has an object to overcome the above-mentioned drawbacks of the prior art by enabling to separate an input TS containing any elementary streams not conforming to MPEG-2 into elementary streams and re-multiplex the elementary streams.

The present invention has another object to permit to change the bit rate of a TS having multiplexed therein a plurality of elementary streams besides video streams.

The above object can be attained by providing a transcoder for generating, from a first multiplexed stream, a second multiplexed stream, including according to the present invention:

means for separating a first elementary stream from the first multiplexed stream supplied;

means for converting the first elementary stream separated by the separating

means by a predetermined method to a signal;

means for packetizing the signal converted by the converting means to generate a first packet;

means for storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and

means for multiplexing, based on the timing information stored in the storing means, the first packet generated by the packetizing means and a second packet containing the second elementary stream to generate the second multiplexed stream.

In the above transcoder according to the present invention, the converting means includes:

means for decoding the first elementary stream separated by the separating means to generate an original signal corresponding to the first elementary stream; and

means for encoding the original signal generated by the decoding means at a predetermined bit rate.

Also in the above transcoder, the converting means converts, by a predetermined method, codes forming the first elementary stream separated by the separating means.

Also in the above transcoder, the multiplexing means multiplexes, based on the timing information stored in the storing means, the second packet to the second multiplexed stream at a time corresponding to the time at which the second packet

appears in the first multiplexed stream.

Also the above object can be attained by providing a transcoding method for generating, from a first multiplexed stream, a second multiplexed stream, including, according to the present invention, steps of:

separating a first elementary stream from the first multiplexed stream supplied;

converting the first elementary stream separated at the separating step by a predetermined method to a signal;

packetizing the signal converted at the converting step to generate a first packet;

storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and

multiplexing, based on the timing information stored at the storing step, the first packet generated at the packetizing step and a second packet containing the second elementary stream to generate the second multiplexed stream.

In the above transcoding method according to the present invention, the converting step includes steps of:

decoding the first elementary stream separated at the separating step to generate an original signal corresponding to the first elementary stream; and

encoding the original signal generated at the decoding step at a predetermined bit rate.

Also in the above transcoding method, at the converting step, codes forming the

first elementary stream separated at the separating step are converted by a predetermined method.

Also in the above transcoding method, at the multiplexing step, the second packet is multiplexed to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream based on the timing information stored at the storing step.

Also the above object can be attained by providing a medium having recorded therein a transcoding program to generate, from a first multiplexed stream, a second multiplexed stream and which is to be executed by a computer, the program including, according to the present invention, steps of:

separating a first elementary stream from the first multiplexed stream supplied;

converting the first elementary stream separated at the separating step by a predetermined method to a signal;

packetizing the signal converted at the converting step to generate a first packet;

storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and

multiplexing, based on the timing information stored at the storing step, the first packet generated at the packetizing step and a second packet containing the second elementary stream to generate the second multiplexed stream.

In the above medium according to the present invention, the converting step in



the program includes steps of:

decoding the first elementary stream separated at the separating step to generate an original signal corresponding to the first elementary stream; and encoding the original signal generated at the decoding step at a predetermined bit rate.

Also in the above medium, at the converting step in the program, codes forming the first elementary stream separated at the separating step are converted by a predetermined method.

Also in the above medium, at the multiplexing step in the program, the second packet is multiplexed to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream based on the timing information stored at the storing step.

These objects and other objects, features and advantages of the present invention will become more apparent from the following detailed description of the preferred embodiments of the present invention when taken in conjunction with the accompanying drawings.

#### Brief Description of the Drawings

FIG. 1 is a block diagram of a conventional bit rate changer, showing an example of its construction.

FIG. 2 is a block diagram of an embodiment of the transcoder according to the present invention.

FIG. 3 is a block diagram of a code converter included in the transcoder in FIG.

2.

FIG. 4 is a block diagram of a TS recorder using the transcoder in FIG. 2, showing an example of its construction.

FIG. 5 explains the operation of the TS recorder in FIG. 4.

FIG. 6 is a flow chart of operations effected in switching control by a controller included in the transcoder in FIG. 2.

FIG. 7 is a block diagram of another embodiment of the transcoder according to the present invention.

FIG. 8A shows a personal computer incorporating a recording medium having stored therein a transcoding program.

FIG. 8B explains the recording medium used to install the transcoding program into the personal computer and enable the execution of the program.

FIG. 8C explains a system used to install the transcoding program into the personal computer and enable the execution of the program.

FIG. 9 is a block diagram of the personal computer, showing its internal construction.

#### Best Mode for Carrying Out the Invention

Referring now to FIG. 2, there is illustrated in the form of a block diagram an embodiment of the transcoder according to the present invention. This transcoder is to generate, from a first transport stream TS supplied as an input, a second transport

stream TS'. The first transport stream TS is a one having multiplexed therein a video signal transport stream packet as a first elementary stream (will be referred to as "video TS packet VTSP" hereinafter) and non-video TS packets UVTSP including audio stream and other data stream.

As shown in FIG. 2, the transcoder includes a video TS packet separator 10 to separate the video TS packet VTSP from the first transport stream TS supplied from an input terminal IN, a code converter 12 to convert, by a predetermined method, codes forming the video TS packet VTSP separated by the video TS packet separator 10, a TS packet generator 15 to generate a code-converted video TS packet VTSP' by packetizing the signal whose codes have been converted by the code converter 12, a TS timing manager 18 to store timing information indicating a time at which the packet including the non-video TS packets UVTSP forming the first transport stream TS appears in the first transport stream TS, and a switch 16 to generate, and deliver at an output terminal OUT, the second transport stream TS' by multiplexing the video TS packet VTSP' generated by the TS packet generator 15 and non-video TS packet UVTSP based on the timing information stored in the TS packet timing manager 18. The switch 16 functions as a multiplexer under the control of a controller 19 which will further be described later.

The transcoder further includes a PES packet analyzer 11 provided between the video TS packet separator 10 and code converter 12, the controller 19 provided between the TS packet timing manager 18 and switch 16, and a TS packet buffer 17.

The video TS packet separator 10 extracts a video TS packet VTSP from the first transport stream TS supplied from the input terminal IN, and provides it to the PES packet analyzer 11. Also, the video TS packet separator 10 extracts, from the first transport stream TS, the non-video TS packet UVTSP and provides it to the TS packet buffer 17. Further, the video TS packet separator 10 supplies the TS packet timing manager 18 with timing information indicating a time at which the non-video TS packet UVTSP has been supplied as an input (information indicating a time at which the non-video TS packet appears in the first transport stream TS).

The PES packet analyzer 11 uses the pay load of the video TS packet VTSP supplied from the video TS packet separator 10 to form a PES (packetized elementary stream) and provides the coder converter 12 with a encoded video signal being the pay load of the PES packet (will be referred to as "encoded video signal" hereinafter). Also the PES packet analyzer 11 provides the code converter 12 with a PTS (presentation time stamp) of the encoded video signal. Note here that the PTS is a parameter described in the header of the PES packet and indicates a time on the time base when the encoded video signal is to be reproduced.

The code converter 12 converts, by the predetermined method, the encoded video signal generated by the PES packet analyzer 11 from the video TS packet VTSP separated by the video TS packet separator 10. For example, when the transcoder is used as a bit rate converter shown in FIG. 1, the code converter 12 decodes the encoded video signal, then re-encodes it at a predetermined bit rate (compression rate)

and supplies it to the TS packet generator 15. Also, the transcoder may convert codes concerning copyright protection information, for example, appended to the encoded video signal.

First, bit rate conversion will be described below. In this case, the code converter 12 includes an MPEG video decoder 13 and MPEG video encoder 14 as shown in FIG. 3. The MPEG video decoder 13 decodes the encoded video signal supplied from the PES packet analyzer 11, and provides the decoded video signal to the MPEG video encoder 14. The MPEG video decoder 13 will also provide the MPEG video encoder 14 with a PTS (presentation time stamp) corresponding to the video signal. The MPEG video encoder 14 re-encodes, at a predetermined bit rate (compression rate), the video signal supplied from the MPEG video decoder 13, and provides the TS packet generator 15 with the re-encoded video signal along with a PTS corresponding to the re-encoded video signal. Under the control of the controller 19, the TS packet generator 15 packetizes the encoded video signal supplied from the MPEG video encoder 14 to generate a video TS packet VTSP', and delivers it at an input terminal a of the switch 16.

The switch 16 is controlled by the controller 19 to turn the output thereof to either the input terminal a or b thereof. The switch 16 will provide, via the output terminal OUT, either the video TS packet VTSP' supplied to the input terminal a thereof from the TS packet generator 15 or the second packet stream TS' generated by time-division multiplexing the non-video TS packets UVTSP, supplied at the input

terminal b thereof from the TS packet buffer 17.

The TS packet buffer 17 stores non-video TS packets UVTSP supplied from the video TS packet separator 10 and delivers them one after another at the input terminal b of the switch 16.

The TS packet timing manager 18 manages timing information supplied from the video TS packet separator 10 and corresponding to the non-video TS packet UVTSP, stored in the TS packet buffer 17 (information indicating a time at which the TS packet UVTSP appears in the TS) and a PCR (program clock reference) indicating a time on the time base of the TS, and supplies them to the controller 19.

The controller 19 controls the TS packet generator 15 and switch 16 based on the timing information and PCR supplied from the TS packet timing manager 18.

The transcoder constructed to convert a bit rate as in the above is used in a transport stream (TS) recorder shown in FIG. 4. The TS recorder records into a recording medium which will further be described later the second transport stream TS' generated by multiplexing the video TS packet VTSP' whose bit rate has been reduced by the code converter 12 of the transcoder, and non-video TS packet UVTSP.

The TS recorded shown in FIG. 4 includes a transcoder as shown in FIG. 1 and a recording system including a bit stream purser 20 to recording medium 28.

The bit stream purser 20 reads a PCR from TS packets supplied one after another from the switch 16 and provides them to a PLL (phase locked loop) block 21 and other to an Arrival\_time stamp append block 23. The PLL block 21 locks a 27-

MHz clock of the recording system to the PCR supplied from the bit stream purser 20. The recording system also includes a time stamp generator 22 which generates a time stamp based on a count of 27-MHz clocks.

When providing a TS packet supplied from the bit stream purser 20 to a smoothing buffer 24 also included in the recording system, the Arrival\_time stamp append block 23 appends to the TS packet a time stamp indicating a time at which a first byte of the TS packet is to be provided to the smoothing buffer 24. The smoothing buffer 24 stores the TS packet having the time stamp appended thereto, and provides it in units of a predetermined data amount appropriately to an error correction block 25 also included in the recording system. The error correction block 25 appends an error correction code to the TS packet supplied from the smoothing buffer 24 and provides it to a modulator 26 also included in the recording system. The modulator 26 modulates the TS packet supplied from the error correction block 25 by a predetermined method, and provides it to a recorder 27 also included in the recording system. The recorder 27 records the modulated TS packet supplied from the modulator 27 to the recording medium 28.

The transcoder used in the TS recorder functions as will be described below with reference to FIG. 5. Note that actually, TS packets shown in FIG. 5D are delayed relative to TS packets shown in FIGS. 5A to 5C for time lengths of the operations of the PES packet analyzer 11 to TS packet generator 15. The non-video TS packets UVTSP are buffered in the TS packet buffer 17 and provided at an appropriate time

(as shown in FIG. 5E) to synchronize these TS packets at each time indicated with a broken line in FIG. 5.

When a TS having time-division multiplexed therein video TS packets VTSP (indicated with “V” in FIG. 5) shown in FIG. 5A and non-video TS packets UVTSP, is supplied from the video TS packet separator 10 to the video TS packet separator 10, the latter will extract the video TS packets VTSP from the TS as shown in FIG. 5B, and provide them to the PES packet analyzer 11. The video TS packet separator 10 will also extract the non-video TS packets UVTSP from the TS as shown in FIG. 5C, and provide them to the TS packet buffer 17. At the same time, the video TS packet separator 10 will provide the TS packet timing manager 18 with information of a time at which the non-video TS packet UVTSP has been supplied.

The PES packet analyzer 11 uses the pay load of the video TS packet VTSP supplied from the video TS packet separator 10 to provide a PES packet, and provides an encoded video signal being the pay load to the MPEG video decoder 13 in the code converter 12. The MPEG video decoder 13 decodes the encoded video signal supplied from the PES packet analyzer 11, and provides the decoded video signal to the MPEG video encoder 14.

The MPEG video encoder 14 re-encodes the video signal supplied from the MPEG video decoder 13 at a predetermined bit rate (half of the bit rate of the TS packets shown in FIG. 5B when the video signal is the TS packets shown in FIG. 5D), and provides it, along with PTS to which the encoded video signal corresponds, to the



TS packet generator 15. Under the control of the controller 19, the TS packet generator 15 packetizes the encoded video signal from the MPEG video encoder 14 as shown in FIG. 5D, and provides it to the input terminal a of the switch 16.

On the other hand, the TS packet buffer 17 supplies the non-video TS packets to the input terminal b of the switch 16 with a delay of a predetermined time as shown in FIG. 5E.

The controller 19 controls the switch 16 to turn the output thereof to its input terminal a or b (as will further be described later with reference to the flow chart of FIG. 6).

Thus, the video TS packets VTSP' and non-video TS packets UVTSP are time-division multiplexed as shown in FIG. 5F are provided from the switch 16 to the bit stream purser 20 located downstream of the switch 16.

The TS packets not continuous to each other as shown in FIG. 5F will be rearranged to be close to each other by the downstream smoothing buffer 24 in the recording system shown in FIG. 4 and then recorded in the recording medium 28.

Next, the switching by the controller 19 will be described herebelow with reference to the flow chart in FIG. 6. First at step S1, the controller 19 judges, based on the timing information and PCR supplied from the TS packet timing manager 18, whether the present time (elapsed from a time when the video TS packet separator 10 was supplied with TS) is a time to provide non-video TS packets UVTSP on the time base of the TS. If it determines that now is not any time to provide the non-video TS

packet UVTSP, it goes to step S2.

At step S2, the controller 19 judges whether the TS packet generator 15 has been supplied with an encoded video signal to be TS-packetized from the MPEG encoder 14. If it determines that the encoded video signal to be TS-packetized is not supplied, it goes to step S3.

At step S3, the TS packet generator 15 will be controlled by the controller 19 to packetize the encoded video signal from the MPEG video encoder 14 and provide it to the input terminal a of the switch 16. The switch 16 will be controlled by the controller 19 to turn an output thereof to the input terminal a and provide the video TS packet VTSP' supplied from the TS packet generator 15 to the downstream bit stream purser 20 shown in FIG. 4.

At step S5, the controller 19 judges whether all TS packets have been provided to the stage downstream of the switch 16. If it determines that all the TS packets have not yet been provided, it goes back to step S1.

If it is determined at step S1 that the present time is a time to provide the non-video TS packet UVTSP, the controller 19 will go to step S4. At step S4, the switch 16 is controlled by the controller 19 to turn the output thereof to the input terminal b and provide the non-video TS packet UVTSP supplied from the TS packet buffer 17 to the bit stream purser 20 located downstream of the switch 16.

Thereafter, the controller 19 will go back to step S1 until it determines at step S5 that all the TS packets have been provided, and the operations at steps S1 and

subsequent steps are repeated. When the controller 19 determines that all the TS packets have been provided, the switching control will be ended.

As in the above, the transcoder functioning to convert a bit rate can convert only the bit rate of video streams contained in a TS to generate a new TS whether or not non-video elementary streams, contained in the TS are conforming to MPEG-2.

Also the transcoder can convert the bit rate of video streams contained in a TS irrespectively of the number of non-video elementary streams contained in the TS to generate a new TS.

The TS recorder can convert only the bit rate of video streams contained in a TS to generate and record a new TS whether or not non-video elementary streams contained in the TS are conforming to MPEG-2.

Also the TS recorder can convert the bit rate of video streams contained in a TS irrespectively of the number of non-video elementary streams to generate and record a new TS.

Note that the present invention is applicable to decoding of a video bit stream forming an input multiplexed stream to generate an original signal, and re-encoding of the original signal to convert the bit rate of the video bit stream forming the input multiplexed stream as well as to converting of codes appended to the video bit stream by a predetermined method, re-appending the converted code to the video bit stream and providing them .

For example, the present invention is applicable to conversion by a

predetermined method of a code concerning a copyright protection information appended to a video bit stream forming an input multiplexed stream as shown in FIG. 7. The transcoder in FIG. 7 is different from that shown in FIG. 2 in that a copyright protection information converter 30 is used instead of the code converter 12 in FIG. 12, the output of the PES packet analyzer 11 is analyzed by an analyzer 31 and code conversion in the copyright protection information converter 30 is controlled according to the result of analysis from the analyzer 31.

The analyzer 31 analyzes the output of the PES packet analyzer 11 to detect copyright protection information such as copy control information (CCI), water mark signal, duplication generation information limiting the number of duplications, etc. embedded in an input video signal. The copyright protection information converter 30 updates, based on the result of analysis from the analyzer 31, codes concerning the copyright protection information by a predetermined method and provides an updated video stream.

For example, when the analyzer 31 detects duplication generation information, the copyright protection information converter 30 will count up the duplication generation information by one, if the generation limitation is within an allowable range, append the count to the video transport packet VTSP correspondingly to the PTS, and send the packet to the TS packet generator 15.

On the other hand, when the generation limitation is beyond the allowable range, the copyright protection information converter 30 will not provide the video TS

packet VTSP to the TS packet generator 15.

Thus, the transcoder shown in FIG. 7 can provide a management to protect the copyright of the video transport stream.

The transcoder shown in FIG. 7 may be combined with the recording system including the bit stream purser 20 thru the recording medium 28 shown in FIG. 4 to construct a TS recorder which will record TS including a video stream whose copyright is protected. Note that the TS recorder cannot record any TS depending upon the type of the copyright protection information, for example, when the generation limitation is beyond the allowable range.

Note that the present invention is applicable to conversion of the bit rate of a video stream included in a program stream or to conversion of a code appended to a video bit stream by a predetermined method.

Also the present invention is applicable to conversion of the bit rate of an audio stream in an TS or conversion of an audio stream in a TS by a predetermined method, and then multiplexing the audio stream with other elementary stream to generate a new TS.

Further the present invention is applicable to conversion of the bit rate of a plurality of elementary streams such as both a video stream and audio stream or conversion of a plurality of elementary streams by a predetermined method, and then multiplexing them with other elementary stream to generate a new TS.

The aforementioned series of processes can be executed by a hardware and also

by a software. For execution of the series of processes by a software, a program forming the software is installed in a computer built in a TS recorded as a dedicated hardware, or a general-purpose personal computer capable of performing a variety of functions with a variety of programs corresponding to the programs installed therein.

The medium according to the present invention having recorded therein a program intended for execution of the above series of processes and which is to be installed in a general-purpose personal computer which can execute the program, will be described herebelow with reference to FIGS. 8A to 8C.

The program can be provided to the user as being previously installed in a hard disc 302 or semiconductor memory 303, as a recording medium, incorporated in a personal computer 301, as shown in FIG. 8A.

Alternatively, the program can be provided to the user as a package software as being provisionally or permanently stored in a recording medium such as a floppy disc 311, CD-ROM (compact disc-read-only memory) 312, MO (magneto-optical) disc 313, DVD (digital versatile disc) 314, magnetic disc 315 or semiconductor memory 316, as shown in FIG. 8B.

Further, as shown in FIG. 8C, the program can be transferred by radio to the personal computer 301 from a download site 321 via a satellite 322 or by cable or radio to the personal computer 301 from the download site 321 via a network 331 such as local area network (LAN) or Internet, and stored in the hard disc 302 or the like incorporated in the personal computer 301.

The term “medium” referred to herein means a broad concept including all these media.

The personal computer 301 incorporates a CPU (central processing unit) 342 as shown in FIG. 9 for example. The CPU 342 has an input/output interface 345 connected thereto via a bus 341. When supplied with a user’s command via the input/output interface 345 from an input unit 347 including a keyboard, mouse, etc., the CPU 342 responds to the command to load into a RAM (random access memory) 344, and execute, a program stored in an ROM (read-only memory) 343 corresponding to the semiconductor memory 303 shown in FIG. 8A, a program transferred from the satellite 322 or network 331, received by the communication unit 348 and installed in the hard disc 302, or a program read out from the floppy disc 311, CD-ROM 312, MO disc 313, DVD 314 or magnetic disc 315 loaded in a drive 349 and installed in the hard disc 302. Further, the CPU 342 provides a result of a process executed thereby to a display unit 346 made of a LCD (liquid crystal display) or the like as necessary via the input/output interface 345 for example.

Note that in this specification, steps of describing the program provided to the user as stored in the medium include processes effected in a time series in the described order as well as processes effected in parallel or individually.

Note that the term “system” referred to herein means an entire assembly of a plurality of units.

Industrial Applicability

As having been described in the foregoing, according to the present invention, a first elementary stream is code-converted to generate a first packet. Also, timing information indicating a time at which a second packet forming a second elementary stream appears in a multiplexed stream is stored. The generated first packet and second packet are multiplexed based on the timing information, so the code of only the first elementary stream can be converted to generate a new second multiplexed stream whether or not the second elementary stream is conforming to MPEG-2. Also, it is possible to convert the codes of a TS having multiplexed therein a plurality of elementary streams in addition to video streams.



## CLAIMS

1. A transcoder for generating, from a first multiplexed stream, a second multiplexed stream, comprising:

means for separating a first elementary stream from the first multiplexed stream supplied;

means for converting the first elementary stream separated by the separating means by a predetermined method to a signal;

means for packetizing the signal converted by the converting means to generate a first packet;

means for storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and

means for multiplexing, based on the timing information stored in the storing means, the first packet generated by the packetizing means and a second packet containing the second elementary stream to generate the second multiplexed stream.

2. The apparatus as set forth in Claim 1, wherein the converting means includes means for decoding the first elementary stream separated by the separating means to generate an original signal corresponding to the first elementary stream, and means for encoding the original signal generated by the decoding means at a predetermined bit rate.

3. The apparatus as set forth in Claim 1, wherein the converting means converts, by a predetermined method, codes forming the first elementary stream separated by the separating means.
4. The apparatus as set forth in Claim 1, wherein the multiplexing means multiplexes, based on the timing information stored in the storing means, the second packet to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream.
5. The apparatus as set forth in Claim 1, wherein the first elementary stream is a video stream.
6. A transcoding method for generating, from a first multiplexed stream, a second multiplexed stream, comprising the steps of:
  - separating a first elementary stream from the first multiplexed stream supplied;
  - converting the first elementary stream separated at the separating step by a predetermined method to a signal;
  - packetizing the signal converted at the converting step to generate a first packet;
  - storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and
  - multiplexing, based on the timing information stored at the storing step, the first packet generated at the packetizing step and a second packet containing the second elementary stream to generate the second multiplexed stream.

7. The method as set forth in Claim 6, wherein the converting step comprises the steps of:

decoding the first elementary stream separated at the separating step to generate an original signal corresponding to the first elementary stream; and  
encoding the original signal generated at the decoding step at a predetermined bit rate.

8. The method as set forth in Claim 6, wherein at the converting step, codes forming the first elementary stream separated at the separating step are converted by a predetermined method.

9. The method as set forth in Claim 6, wherein at the multiplexing step, the second packet is multiplexed, based on the timing information stored at the storing step, to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream.

10. The method as set forth in Claim 6, wherein the first elementary stream is a video stream.

11. A medium having recorded therein a transcoding program to generate, from a first multiplexed stream, a second multiplexed stream and which is to be executed by a computer, the program comprising the steps of:

separating a first elementary stream from the first multiplexed stream supplied;  
converting the first elementary stream separated at the separating step by a predetermined method to a signal;

packetizing the signal converted at the converting step to generate a first packet; storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and

multiplexing, based on the timing information stored at the storing step, the first packet generated at the packetizing step and a second packet containing the second elementary stream to generate the second multiplexed stream.

12. The medium as set forth in Claim 11, wherein the converting step in the program comprises the steps of:

decoding the first elementary stream separated at the separating step to generate an original signal corresponding to the first elementary stream; and

encoding the original signal generated at the decoding step at a predetermined bit rate.

13. The medium as set forth in Claim 11, wherein at the converting step in the program, codes forming the first elementary stream separated at the separating step are converted by a predetermined method.

14. The medium as set forth in Claim 11, wherein at the multiplexing step in the program, the second packet is multiplexed to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream based on the timing information stored at the storing step.

15. The medium as set forth in Claim 11, wherein the first elementary stream is

a video stream.

## ABSTRACT

In a video TS packet separator, a video TS packet VTSP is extracted from a TS (transport stream). In a PES packet analyzer through a TS packet generator, the video TS packet is decoded, re-encodes at a predetermined bit rate, TS-packetized and supplied to a switch input terminal. The switch input terminal is also supplied with non-video TS packets UVTSP from a TS packet buffer. A controller is provided to control the TS packet generator and switch based on timing information and PCR from a TS packet timing manager. Thus, codes in the TS can be converted.



(43) 国際公開日  
2001年1月11日 (11.01.2001)

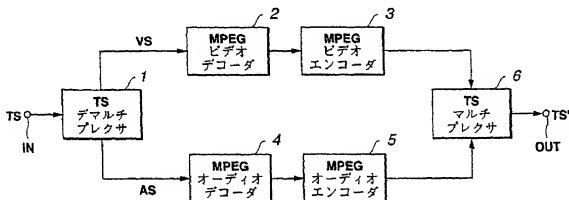
PCT

(10) 国際公開番号  
WO 01/03305 A1

- (51) 国際特許分類: H03M 7/00, Motoki) [JP/JP]; 〒141-0001 東京都品川区北品川6丁目7番35号 ソニー株式会社内 Tokyo (JP).  
H04L 13/00, 12/56, H04N 7/24
- (21) 国際出願番号: PCT/JP00/04375
- (22) 国際出願日: 2000年6月30日 (30.06.2000)
- (25) 国際出願の言語: 日本語
- (26) 国際公開の言語: 日本語
- (30) 優先権データ: 特願平11/184681 1999年6月30日 (30.06.1999) JP
- (71) 出願人(米国を除く全ての指定国について): ソニー株式会社 (SONY CORPORATION) [JP/JP]; 〒141-0001 東京都品川区北品川6丁目7番35号 Tokyo (JP).
- (72) 発明者: および
- (75) 発明者/出願人(米国についてのみ): 加藤元樹 (KATO,
- (74) 代理人: 小池 晃, 外(KOIKE, Akira et al.); 〒105-0001 東京都港区虎ノ門二丁目6番4号 第11森ビル Tokyo (JP).
- (81) 指定国(国内): CN, JP, KR, US.
- (84) 指定国(広域): ヨーロッパ特許 (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).
- 添付公開書類:  
— 国際調査報告書
- 2文字コード及び他の略語については、定期発行される各PCTガゼットの巻頭に掲載されている「コードと略語のガイダンスノート」を参照。

(54) Title: METHOD AND APPARATUS FOR TRANSCODING, AND MEDIUM

(54) 発明の名称: トランスコード装置及び方法、並びに媒体



- 1...TS DEMULTIPLEXER
- 2...MPEG VIDEO DECODER
- 3...MPEG VIDEO ENCODER
- 4...MPEG AUDIO DECODER
- 5...MPEG AUDIO ENCODER
- 6...TS MULTIPLEXER

(57) Abstract: A video TS packet separator extracts a video TS packet (VTSP) from a TS. A PES packet analyzer and a TS packet generator decode the video TS packet, encode again at a predetermined bit rate, and supply the TS packet to an input terminal of a switch. The input terminal of the switch receives a TS packet (UVTSP) other than video from a TS packet buffer. A control section controls the TS packet generator and the switch according to the timing information and PCR from a TS packet timing manager. Sign conversion is thus carried out for a transport stream.

[続表有]

WO 01/03305 A1

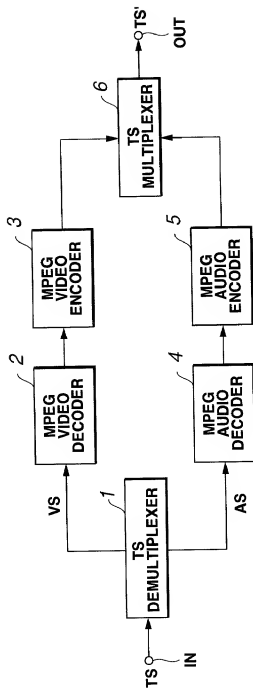


FIG.1



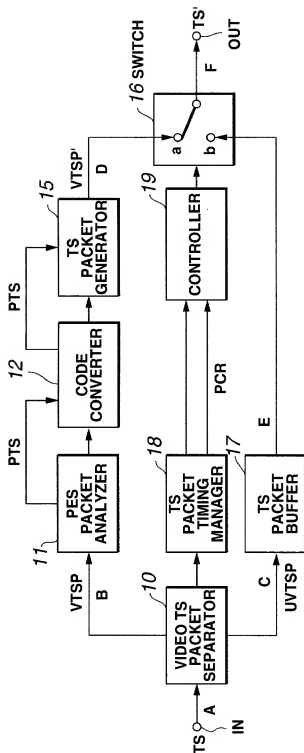


FIG.2

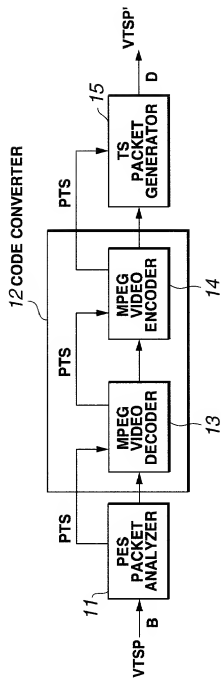


FIG.3

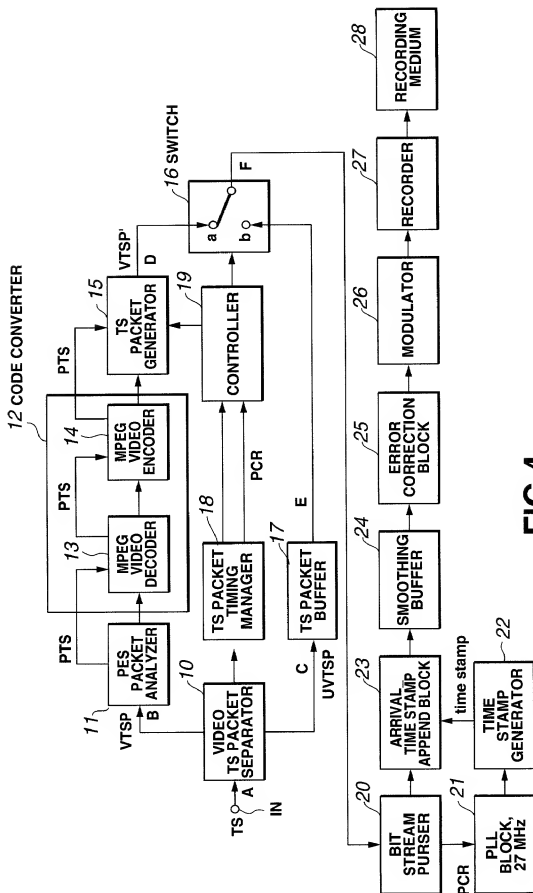


FIG. 4

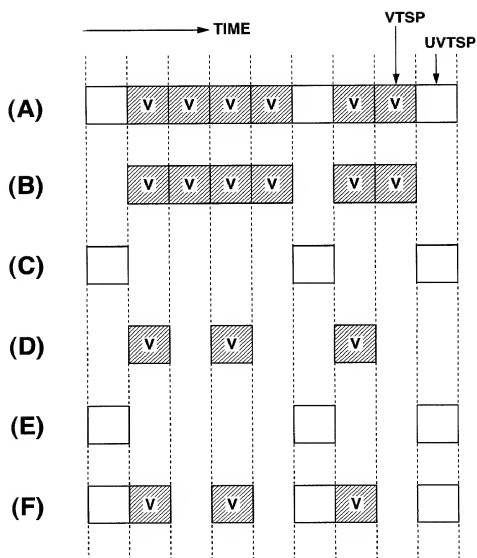


FIG.5

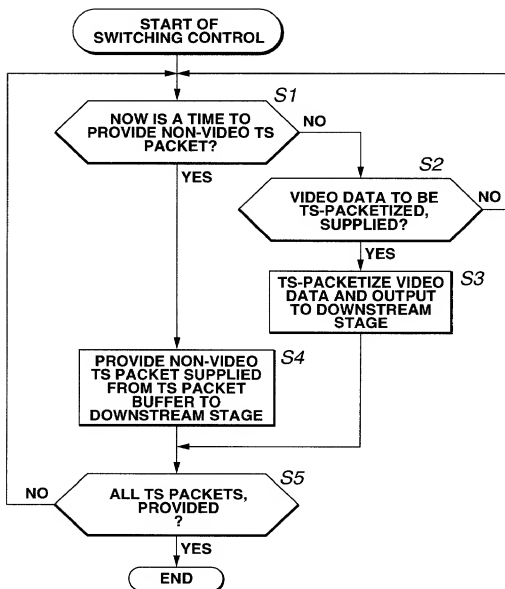


FIG.6

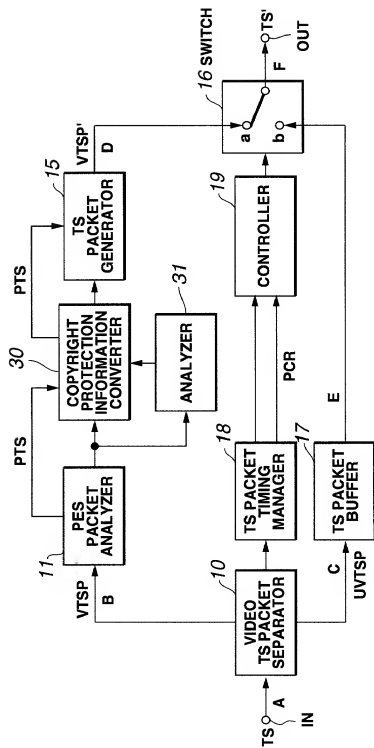
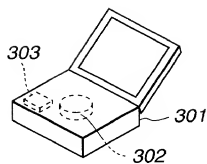
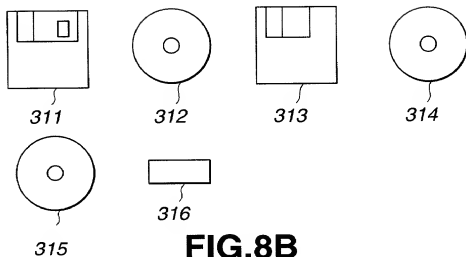
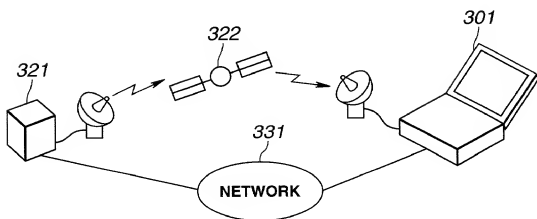
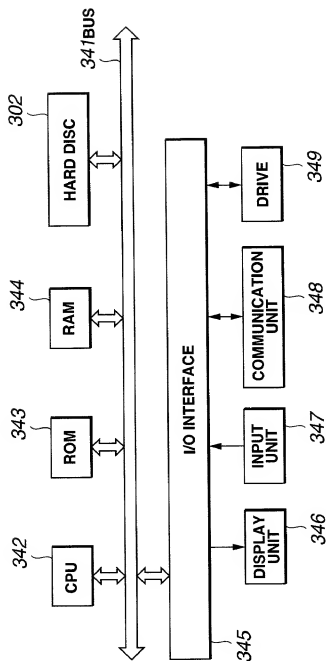


FIG. 7

8/9

**FIG. 8A****FIG. 8B****FIG. 8C**



**FIG.9**



500P070645-08

## Declaration and Power of Attorney for Patent Application

## 特許出願宣言書及び委任状

## Japanese Language Declaration

## 日本語宣言書

私は、以下に記名された発明者として、ここに下記の通り宣言する：

As a below names inventor, I hereby declare that:

私の住所、郵便の宛先そして国籍は、私の氏名の後に記載された通りである。

My residence, post office address and citizenship are as stated next to my name:

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled.

☐ 下記の名称の発明について、特許請求範囲に記載され、且つ特許が認められている発明主題に関して、私は、最初、最先且つ唯一の発明者である（唯一の氏名が記載されている場合）か、或いは最初、最先且つ共同発明者である（複数の氏名が記載されている場合）と信じている。

TRANSCODING APPARATUS, METHOD AND MEDIUM

the specification of which is attached hereto unless the following box is checked:

上記発明の明細書はここに添付されているが、下記の欄がチェックされている場合は、この限りでない：

☒ was filed on June 30, 2000  
as United States Application Number of  
PCT International Application Number PCT/JP00/04375  
and was amended on  
(if applicable).

☐ の日に出版され、  
この出願の米国出願番号またはPCT国際出願番号は、  
であり、且つ  
の日に修正された出願（該当する場合）

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、上記の修正書によって修正された、特許請求範囲を含む上記明細書を検討し、且つ内容を理解していることをここに表明する。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

私は、送付規則法内第37編規則1.56に定義されている、特許性について重要な情報を開示する義務があることを認める。

Burden Hour Statement: This form is estimated to take 0.4 hours to complete. Time will vary depending upon the need of the individual case. Any comments on the amount of time you are required to complete this form should be sent to Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231 DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner of Patents and Trademarks, Washington, DC 20231.

Japanese Language Declaration

日本語宣言書

私は、ここに、以下に記載した外国での特許出願または発明者証の出願、或いは米国以外の少なくとも一國を指定している米国法典第35編第365条(a)によるPCT国際出願について、同第119条(e)の項又は第365条(b)の項に基づいて優先権を主張するとともに、優先権を主張する本出願の出願日より前の出願を有する外国での特許出願または発明者証の出願、或いはPCT国際出願については、いかなる出願も、下記の特典をチェックすることにより示した。

Prior Foreign Application(s)

外国での先行出願

11-184681

(Number)  
(番号)

Japan  
(Country)  
(国名)

PCT/JP00/04375

(Number)  
(番号)

PCT  
(Country)  
(国名)

(Number)  
(番号)

(Country)  
(国名)

(Number)  
(番号)

(Country)  
(国名)

(Number)  
(番号)

(Country)  
(国名)

(Number)  
(番号)

(Country)  
(国名)

私は、ここに、下記のような米国仮特許出願についても、その米国法典第35編119条(e)項の利益を主張する。

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

私は、ここに、下記のような米国出願についても、その米国法典第35編第120条に基づく利益を主張し、又米國を指定するいかなるPCT国際出願についても、その同第365条(e)に基づき利益を主張する。また、本出願の各特許請求の範囲の主題が、米国法典第35編第112条第1段に規定された態様で、先行する米国出願又はPCT国際出願に開示されていない場合においては、その先行出願の出願日と本国内出願日またはPCT国際出願日との間の期間中に入手された情報で、連邦裁判法典第37編規則1.56に定義された特許性に関わる重要な情報について開示義務があることを承諾する。

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

私は、ここに表明された私自身の知識に係わる陳述が真実であり、且つ情報と偽ることに基づく陳述が、真実であると信じられることを宣言し、さらに、故意に虚偽の陳述などを行った場合は、米国法典第18編第1001条に基づき、罰金または拘禁、若しくはその両方より処罰され、またそのような故意による虚偽の陳述は、本出願またはそれに対して発行されるいかなる特許も、その有効性に問題が生ずることを理解した上で陳述が行われたことを、ここに宣言する。

I hereby claim foreign priority under Title 35, United States Code, Section 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT international application having a filing date before that of the application for which priority is claimed.

Priority Not Claimed

優先権主張なし

30 June 1999  
(Day/Month/Year Filed)

☐

30 June 2000  
(Day/Month/Year Filed)

☐

(Day/Month/Year Filed)

☐

(Day/Month/Year Filed)

☐

(Day/Month/Year Filed)

☐

(Day/Month/Year Filed)

☐

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any united States provisional application(s) listed below.

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of application.

(Status: Patented, Pending, Abandoned)  
(現状: 特許許可、係属中、放棄)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

## Japanese Language Declaration

## 日本語宣言書

委任状: 私は本出願を審査する手続を行い、且つ米国特許商標庁との全ての業務を遂行するために、記名された発明者として、下記の弁護士及び/または弁理士を任命する。(氏名及び登録番号を記載すること)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number)

書類送付先

WILLIAM S. FROMMER, Registration No. 25,506 and  
DENNIS M. SMID, Registration No. 34,930

Send Correspondence to:  
WILLIAM S. FROMMER, Esq.  
c/o FROMMER LAWRENCE & HAUG LLP  
745 Fifth Avenue  
New York, New York 10151

直通電話連絡先: (氏名及び電話番号)

Direct Telephone Calls to:  
(212) 588-0800  
to the attention of:  
WILLIAM S. FROMMER

唯一または第一発明者氏名

Full name of sole or first inventor

発明者の署名

日付

Motoki KATO

inventor's signature

Date

住所

Residence

国籍

Kanagawa Japan

Citizenship

郵便の宛先

Japan

Post Office Address

c/o Sony Corporation, 7-35, Kitashinagawa 6-chome,  
Shinagawa-ku, Tokyo 141-0001 Japan

第二共同発明者がある場合、その氏名

full name of second joint inventor, if any

第二共同発明者の署名

日付

Second Inventor's signature

Date

住所

Residence

国籍

Citizenship

郵便の宛先

Post Office Address

(第三以下の共同発明者についても同様に記載し、署名をすること)

(Supply similar information and signature for third and subsequent joint inventors)